

**Two Day Workshop
On
VLSI design and image processing**

3-4 Feb 2017

Registration form

Send the duly filled following details to the following mail id:

Elsdworkshop2017@gmail.com

Name:.....

Designation:.....

Department:.....

Qualification:.....

Experience(in years):

Teaching:.....

Industry:.....

Organization:.....

Accommodation required: YES/NO

Mailing address:.....

Telephone(with code)

(Office):.....

(R)/Mobile:.....

Email:.....

Or

Browse the following link and fill the google form to register for the workshop

<https://goo.gl/yFf0QE>

Visit the facebook page for updates:

<https://www.facebook.com/elsdworkshop/>

Patron

Prof. Kailash Chandra Sharma
Vice-Chancellor, Kurukshetra University,
Kurukshetra

Coordinators

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Chairperson
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Kurukshetra University, Kurukshetra

Prof. Anil Vohra

Electronic Science Department
Dean, Academic Affairs
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Kurukshetra University, Kurukshetra

Local Organizing Committee

Dr. Sacheen Kumar
Mr. Anil Kumar
Mr. Rakesh Singh
Mr. Sumit Choudhary
Ms. Shruti



**Two Day Sawarn Jayanti Workshop
On
VLSI DESIGN AND IMAGE PROCESSING
3-4 Feb 2017**

Organised by



Department of Electronic Science
Kurukshetra University
Kurukshetra-136119

Under the aegis of:
TEQIP(1.2) world bank project

Venue
Department of Electronic Science
Kurukshetra University, Kurukshetra
&
Senate Hall, Kurukshetra university

About the University

The Kurukshetra University was established in 1956 as a unitary residential university and its foundation stone was laid by late Dr. Rajendra Prasad. Since then the university has made a steady progress to acquire a status of a premiere institution of learning. It is now a multi-faculty university with as many as 40 teaching departments offering various courses in Science, Engineering, Arts, Languages and Humanities. The university has an aesthetically designed and beautifully laid out campus in order to provide necessary impetus to pursue academic activities.

Electronic Science Department

The Electronic Science Department was established in 1990 with the aim of generating specially trained manpower in the highly sophisticated field of microelectronics, integrated circuit design, fabrication and semi-conductor material technology. This department was established with funding from Department of Electronics, Govt. of India, UGC and Haryana state government.

Courses Offered:

- ◆ M.Sc (Electronic Science)
- ◆ M.Tech (Microelectronic and VLSI Design)
- ◆ M.Tech (Nanoscience and Technology)

Design & Simulation Tools Available:

- ◆ CADENCE
- ◆ SILVACO SIMU CAD
- ◆ XILINX
- ◆ IMAGE
- ◆ SILVACO T-CAD SUITE
- ◆ MATLAB

MEMS Design Tools:

- ◆ COVENTORWARE
- ◆ INTELLISUITE
- ◆ COMSOL
- ◆ MEMS+

Nano Material Synthesis Tools:

- ◆ MEDEA

The department has established active collaboration with a number of national laboratories and industries and also offers consultancy services to the nearby electronic industries for testing and development of products. Joint research activities are undertaken with National Physical Laboratory, Central Electronics Engineering Research Institute, Semiconductor Complex Limited, etc. A number of research scholars are working in areas related to physics and technology of integrated circuits, devices and simulation.

About Image Processing

Electronic Science department is running two M.Tech. Courses in the area of Microelectronic & VLSI design and Nano science and technology. The courses of VLSI design and image processing are being taught in M.Tech. Course. The faculty members and research scholars of electronic Science department is proposing a two day workshop in the area of “VLSI design and image processing”. This field has enormous scope in the cutting edge VLSI technology. The latest research in the field of digital signal processing, image processing and VLSI design provides excellent opportunities in the research as well as in industry.

Who can attend?

Faculty from engineering colleges as well as those involved in the teaching of Postgraduate courses (M.Sc./M.Tech.) in universities, Research Scholars also encouraged to participate.

COURSE CONTENTS

- ◆ Image processing
- ◆ Digital signal processing
- ◆ VLSI design in DSP
- ◆ Hands on training on FPGA boards

The experts from premier institutes/national labs will deliver talks and will also provide hands-on training to the participants. The list of tentative speakers is following

RESOURCE PERSONS

- ◆ Dr. Ajay Kumar, Scientist IRDE, Dehradun
- ◆ Dr. Sanjay Singh, Scientist, CEERI Pilani
- ◆ Dr. Ravi Saini, Scientist, CEERI Pilani
- ◆ Dr. Ghanekar, Prof. NIT, Kurukshetra
- ◆ Dr. Amit Kumar Aggarwal, Scientist, IRDE, Dehradun

Important Dates

Last date for registration : 27 January, 2017

Registration fee

Academic staff : Rs. 1000/-

Research Scholar : Rs. 500/-

Workshop registration fees can be submitted through online mode only. Followings are the details for online transaction:

Account Number:

36409195046

Bank Name: SBI, Kurukshetra university

IFSC Code: SBIN0001600

Note: requests for registration after last date will not be entertained.

Accommodation: may be made available on request. The participants need to pay on their own.

For further enquiries and communication, Please contact:

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