

Final List of the selected candidates for admission to M.Tech Microelectronics & VLSI Design course, session 2010-11.

All Admissions are provisional

S.No.	Roll No	Name of candidate	Father's Name	Q.Exam	%GATE	Ent Test
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A. All India Category -2

1	10170843	Sudha Kumari	Ram Subhag Singh	69.98	26.67	0
2	10170734	Tarannum Parvin	Sh. Nafees Ahmad	68.38	26.33	0

B. Haryana Domicile – 2

1	1017089	Shaveta	Sh. Prem Lal Aggarwal	72.22	50	
2	1017082	Sumit Choudhary	Sh. Hardiyal Singh	67.46	48	

C. Backward Classes of Haryana - Block A – 2

1	1017080	Smriti	Sh. Raj Kumar	0	38	
2	1017088	Nitin Rohilla	Sh. Suresh Kr. Rohilla	68.21	36	

Selected candidates are requested to submit their original certificates and documents in the office of the undersigned latest by 21.8.2010, 1.00 pm.

Chairman

Copy of the above is forwarded to

1. Dean, Faculty of Science, KUK
2. The Incharge, fee section, K.U.K alongwith admission forms of the candidates for further necessary action. The last date for depositing the dues is 21.8.2010

Chairman

