



KURUKSHETRA UNIVERSITY KURUKSHETRA
 (Established by the State Legislature Act-XII of 1956)
 ("A+" Grade NAAC Accredited)

NOTIFICATION

It is hereby notified that the examinations of the following class/course has been rescheduled as under:

Class	Date of Exams already notified	Sr. No.	Nomenclature of the Paper	Revised Date of Examination
M. Tech Microelectronics & VLSI Design 2nd Sem	13-May-2019	39503	MMVD-203: Analog CMOS Integrated Circuits	15-May-2019
M. Tech Microelectronics & VLSI Design 2nd Sem	17-May-2019	39504	MMVD-204: Verilog Hardware Description Language	20-May-2019

**THE VENUE & TIMING OF EXAMINATION WILL
 REMAIN THE SAME AS NOTIFIED EARLIER**

CONTROLLER OF EXAMINATIONS

Endst.No.CT-IV/2019/

9356-70

Dated: 05-4-19

Copy of the above is forwarded to the following for information and further necessary action:

- 1 Chairperman, Department of Electronic Sc., K.U. Kurukshetra with the request to inform the concerned candidates and display the same on the **Notice Board**.
- 2 Assistant Registrar (Secrecy), K.U. Kurukshetra.
- 3 S.A.O. (Exams.-II), K.U. Kurukshetra with the request to inform the concerned candidates, immediately
- 4 Director, AEIMC, K.U. Kurukshetra with the request to place the same on University website.
- 5 P.A. to Controller of Examinations-I & II, K.U. Kurukshetra.
- 6 Superintendent (Confidential/Practical/UMC), K.U. Kurukshetra.
- 7 CT-II (Conduct Theory Section), K.U. Kurukshetra to inform the concerned **Nodel Centres** accordingly.


Sr. Admn. Officer (Conduct)