

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
(Established by the State Legislature Act XII of 1956)  
("A+" Grade NAAC Accredited)

**Date-Sheet for the M.Sc. Electronics Science & M.Tech. Microelectronics & VLSI Design (I SEMESTER)**  
**Examinations commencing w.e.f. 01-04-2021.**

**(THEORY EXAMINATION)**

**Time of Examination : 9.30 A.M. (MORNING SESSION)**

<b>DATE</b>	<b>SUBJECT &amp; PAPER</b>
	<b>M.Sc. Electronics Science I Semester</b>
<b>1/Apr/2021</b>	EL-11 : Mathematical & Computational Techniques in Electronics (Syll. CBCS wef 2016-17)
<b>5/Apr/2021</b>	EL-12: Physics of Solid State Devices (Syll. CBCS wef 2016-17)
<b>5/Apr/2021</b>	EL-102: Device Electronics of ICS (Old Syllabus)
<b>8/Apr/2021</b>	EL-13: IC Fabrication Technology (Syll. CBCS wef 2016-17)
<b>12/Apr/2021</b>	EL-14: EM Theory & Electronic Communication (Syll. CBCS wef 2016-17)
<b>15/Apr/2021</b>	EL-15: Electronic Instrumentation & Control System (Syll. CBCS wef 2016-17)
	<b>M.Tech Micro &amp; VLSI Design Ist Semester</b>
<b>1/Apr/2021</b>	Paper-MMVD 101: Process Technology for ULSI-I (New)
<b>1/Apr/2021</b>	Paper-MMVD 1.1: Process Technology for ULSI-I (Old)
<b>5/Apr/2021</b>	Paper-MMVD 102: MOSFET Physics and Sub-Micron Device Modeling (New)
<b>5/Apr/2021</b>	Paper-MMVD 1.2: Modeling Techniques for Submicron Devices (Old)
<b>8/Apr/2021</b>	Paper-MMVD 103: VLSI Design (New)
<b>8/Apr/2021</b>	Paper-MMVD 1.3: VLSI Design-system on Silicon (Old)
<b>12/Apr/2021</b>	Paper-MMVD 104: Digital Signal Processing (New)
<b>12/Apr/2021</b>	Paper-MMVD 1.4: Hardware Description Languages (Old)

**NOTE :** Before attempting the paper, the candidate will ensure that he/she has received the correct question-paper.  
No complaint for attempting wrong question-paper by the candidate will be entertained later on.

**KURUKSHETRA**  
**8/Mar/2021**

**CONTROLLER OF EXAMINATIONS**