

KURUKSHETRA UNIVERSITY KURUKSHETRA
(Established by the State Legislature Act XII of 1956)
("A+" Grade NAAC Accredited)

Date-Sheet for the M.Sc. Electronics Science & M.Tech Micro & VLSI Design III SEMESTER (CBCS only)
Examinations to be held in December, 2021.

(THEORY EXAMINATION)

Time of Examination : 10.00 A.M. (MORNING SESSION)

DATE	SUBJECT & PAPER
	M.Sc. Electronics Science III Semester
9-Dec-2021	EL-31: MOS Solid State Circuits (CBCS wef 2021-22)
13-Dec-2021	EL-32: Semiconductor Materials & Device Characterization (CBCS wef 2021-22)
17-Dec-2021	EL-33: Microwave and Optoelectronic Devices (CBCS wef 2021-22)
21-Dec-2021	EL-34: Opt.(i) Custom Microelectronics & ASICs (CBCS wef 2021-22)
24-Dec-2021	EL-35: Opt.(i) Digital Communication (CBCS wef 2021-22)
	M.Tech Micro & VLSI Design 3rd Semester
9-Dec-2021	MMVD-301: Micro Electro Mechanical Systems (MEMS)
13-Dec-2021	MMVD-302: Digital System Testing and Fault Simulation
17-Dec-2021	MMVD-303: Nano Science & Technology

NOTE : **1 Before attempting the paper, the candidate will ensure that he/she has received the correct question-paper. No complaint for attempting wrong question-paper by the candidate will be entertained later on.**

2 The mode of Examination will be intimated one week before commencement of examination by the Result Branch to the candidate through their Admit Card/Roll No., keeping in view the prevailing situation of COVID-19 at that time.

KURUKSHETRA

CONTROLLER OF EXAMINATIONS

12-Nov-2021