AQAR-2020-21

1.3.4: Students undertaking field projects / research projects / internships

Students of the Department/Institute of <u>Electronic Science</u> undertaking field projects /research projects/ internships.

This is to certify that the dissertation/seminar report entitled "Design and Simulation

of MICROPROCESSOR using Verilog" is a bonafide record of project work carried out

by ALKA (Roll No 8080061) under my supervision for partial fulfillment for the award

of postgraduate degree of Master of Science in Electronic Science in the Department

of Electronic Science, Kurukshetra University, Kurukshetra-136119.

Date: 04-07-2021

DR. ANIL VOHRA

R&D Department

Electronics science department

Kurukshetra university,kurukshetra



CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE (विज्ञान तथा प्रौद्योगिकी मंत्रालय / MINISTRY OF SCIENCE & TECHNOLOGY, भारत सरकार / GOVT. OF INDIA) पिलानी, राजस्थान (भारत) / Pilani, Rajasthan - 333031 (INDIA)



Dr. Suchandan Pal Head, Project Monitoring & Evaluation Chairman, International Science & Technology Affairs Group

> No. PME\S\Trg\KUK\2021 Date: 28/06/2021

To,

The Head,
Department of Electronics Science,
Kurukshetra University,
Kurukshetra – 136119

CERTIFICATE OF TRAINING

This is to certify that **Mr. Bhaskar Sharma**, **Roll No. 8080059**, **M.Sc.** (**Electronics Science**) of your Institute has completed his project work online at this Institute during **21**st **January**, **2021 to 28**th **June**, **2021**. His project was on "**Design and Analysis of MEMS Gyroscope**".

His conduct during the training was satisfactory.

Head, PME

प्रमुख पी.एम.ई. / Head PME सीएसआईआर-कंन्द्रीय इतेक्ट्रॉनिकी अमियान्त्रिकी अनुसंघान संस्थान CSIR-Central Electronics Engineering Research Institute पिलानी, राजस्थान / Pilani, Rajasthan-333031



CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE (विज्ञान तथा प्रौद्योगिकी मंत्रालय / MINISTRY OF SCIENCE & TECHNOLOGY, भारत सरकार / GOVT. OF INDIA) पिलानी, राजस्थान (भारत) / Pilani, Rajasthan - 333031 (INDIA)



Dr. Suchandan Pal Head, Project Monitoring & Evaluation Chairman, International Science & Technology Affairs Group

> No. PME\S\Trg\KUK\2021 Date: 30/06/2021

To,

The Head,
Department of Electronics Science,
Kurukshetra University,
Kurukshetra – 136119

CERTIFICATE OF TRAINING

This is to certify that Mr. Gaurav, Roll No. 8080063, M. Sc. (Electronics Science) of your Institute has completed his project work online at this Institute during 21st January, 2021 to 30th June, 2021. His project was on "Alumina Based IR Emitter".

His conduct during the training was satisfactory.

Head, PME

प्रमुख पी.एम.ई./Head PME सीएवआईआर-केन्द्रीय इतेस्ट्रॉनिकी बनियानिकी अनुसंघन संस्थान CSIR-Central Electronics Engineering Research Institute पिलानी, राजस्थान/Pilani, Rajasthan-333031

This is to certify that the dissertation/seminar report entitled "Synthesis and Characterization of Ce and Sr Zno NanoStructures" is a bonafide record of project work carried out by Meenu (Roll No: 16) under my supervision for partial fulfilment for the award of postgraduate degree of Master of Science in Electronic Science in the Department of Electronic Science, Kurukshetra University, Kurukshetra-136119.

Date: 29-07-2021

(Dr.Suresh Kumar)

Professor Department of

Department of Electronic Science Kurukshetra University Kurukshetra-136119

Certificate

It is certified that the dissertation report entitled "Verilog based Microcontroller Design" is bonafide record of project work carried out by Ms. Namita Nandwani under my supervision for partial fulfilment of for the award of postgraduate degree of Master of Science in Electronic Science in Department of Electronic Science, Kurukshetra University, Kurukshetra-136119

Prof. Anil Vohra 28-6-2021

Electronic Science Department

Kurukshetra University Kurukshetra-136119



I hereby certify that the work which is being presented in the final report entitled "INTERFACING OF CAMERA MODULE WITH EMBEDDED BASED MICROCONTROLLER BOARD AND ITS DATA TRANSMISSION AND RECEPTION" in partial fulfilment of the requirements for the award of the Master of Science in Electronic Science and submitted to the Researcher Dr. DHIRAJ in CEERI PILANI is an authentic record of my work carried out by them under my guidance and supervision.

The matter presented in this dissertation has not been submitted by me for the award of any other degree elsewhere

. This is to certify that the above statement made by the candidate is correct to the bes of my knowledge.	
Internal Guide:	Dr. Dhiraj Principal Scientist (CSIR-CEERI)

Place: Date:

This is to certify that the project titled "THz Polarisers", submitted by Nidhi Upadhyay submitted in partial fulfilment of the requirements for the award of the Degree of Masters of Science (M.Sc.) in Department of Electronic Sciences, Kurukshetra University during the training period of 3rd Semester, is an authentic work carried out by her under my supervision and guidance in the Nanotechnology Department, Solid State Physics Laboratory (SSPL), Defence Research And Development Organization (DRDO).

She completed her project successfully in time with due diligence. During the period of her project, she has demonstrated good conduct.

Supervisor

Dr Abhilasha Chouksey Scientist 'D' Nanotechnology group SSPL, DRDO



CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE (विज्ञान तथा प्रौद्योगिकी मंत्रालय / MINISTRY OF SCIENCE & TECHNOLOGY, भारत सरकार / GOVT. OF INDIA) पिलानी, राजस् थन (भारत) / Pilani, Rajasthan - 333031 (INDIA)



Dr. Suchandan Pal Head, Project Monitoring & Evaluation Chairman, International Science & Technology Affairs Group

> No. PME\S\Trg\KuK\2021 Date: 30/06/2021

To,

The Head,
Department of Electronics Science,
Kurukshetra University,
Kurukshetra – 136119

CERTIFICATE OF TRAINING

This is to certify that Mr. Nikhil Aggarwal Roll no. 8080065, M. Sc. (Electronics Science) of your Institute has completed his project work online at this Institute during 21st January, 2021 to 30th June, 2021. His project was on "Designing of LTCC based IR Emitter".

His conduct during the training was satisfactory.

Head, PME

प्रमुख पी.एम.ई./Head PME सीएसवाईआर-केन्द्रीय इतेक्ट्रॉनिकी विभवान्त्रिकी अनुसंघान संस्थान CSIR-Central Electronics Engineering Research Institute पिलानी. राजस्थान/Pilani, Rajasthan-333031



CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE (विज्ञान तथा प्रौद्योगिकी मंत्रालय / MINISTRY OF SCIENCE & TECHNOLOGY, भारत सरकार / GOVT. OF INDIA) पिलानी, राजस् थन (भारत) / Pilani, Rajasthan - 333031 (INDIA)



Dr. Suchandan Pal Head, Project Monitoring & Evaluation Chairman, International Science & Technology Affairs Group

> No. PME\S\Trg\KUK\2021 Date: 29/06/2021

To,

The Head,
Department of Electronics Science,
Kurukshetra University,
Kurukshetra – 136119

CERTIFICATE OF TRAINING

This is to certify that Mr. Parshant, Roll No. 8080052, M. Sc. (Electronics Science) of your Institute has completed his project work online at this Institute during 8th January, 2021 to 20th June, 2021. His project was on "Sample Identification and Collection with a Drone Manipulator Within a Survey Area".

His conduct during the training was satisfactory.

Head, PME

प्रमुख पी.एम.ई./Head PME सीएसआईआर-केन्दीय इतेक्ट्रॉनिकी अभिवान्त्रिकी अनुसंघान संस्थान CSIR-Central Electronics Engineering Research Institute पिलानी. राजस्थान/Pilani, Rajasthan-333031

This is to certify that the dissertation/seminar report entitled "Design and Simulation of DC Motor controller using microcontroller" is a bonafide record of project work carried out by Priyanka Malik (Roll No 8080060) under my supervision for partial fulfillment for the award of postgraduate degree of Master of Science in Electronic Science in the Department of Electronic Science, Kurukshetra University, Kurukshetra-136119.

Date-25-06-2021

DR. ANIL VOHRA

DEPARTMENT OF ELECTRONICS SCIENCE KURUKUSHTERA UNIVERSITY KURUKSHETRA



FFEVER CONSULTING PVT LTD

Dated: 30-June-2021

TO WHOSOEVER IT MAY CONCERN

This is to certify that Mr. Rahul Solanki has joined us as for internship on 20th Jan 2021. During the internship, he had developed good skill in PCB Designing and firmware development of Vehicle Tracking System'.

During his internship with us from Jan 2021 till Jun 2021, Rahul has contributed significantly in PCB designing and firmware development for our various IOT products. He is quick learner & posses high level of self-discipline.

We wish Rahul a huge success in his professional as well as personal life.

For Ffever Consulting Pvt. Ltd.

Authorised Signatory

This is to certify that the dissertation "Design of Low-Pass and High-Pass FIR Filters for Photoplethysmogram Sensor Applications" is a bonafide record of project work carried out by Shriyam Malviya (Roll No.- 19104) under my supervision for partial fulfillment for the award of postgraduate degree of Master of Technology in Microelectronics and VLSI Design in the Department of Electronic Science, Kurukshetra University, Kurukshetra-136119.

नप्रोपाल पाण्डेप

11 August 2021

Dr. Jai Gopal Pandey

Principal Scientist

Central Electronics Engineering Research Institute

Pilani, Rajasthan

This is to certify that the dissertation report entitled "Design and Simulation of FET-based sensors for Chemical/Biochemical sensing applications" is a bonafide record of project work carried out by AJAY KUMAR (Roll No 19101) under my supervision for partial fulfilment for the award of postgraduate degree of Master of Technology in Microelectronics and VLSI Design from Department of Electronic Science, Kurukshetra University Kurukshetra, Haryana-13619.

Date: 22/06/2021

Mr. Soumendu Sinha

Senior Scientist Semiconductor Device Design Group CSIR-Central Electronics Engineering Research Institute Pilani, Rajasthan-333031

This is to certify that the dissertation "Design of a Multi-Stage Multi-Rate Digital Decimation Filter" is a bonafide record of project work carried out by Namika Sharma (Roll No- 19106) under my supervision for partial fulfillment for the award of the postgraduate degree of Master of Technology Microelectronics & VLSI in the Department of Electronic Science, Kurukshetra University, Kurukshetra-136119.

Date:23-07-2021

जयगोरात पाण्डेप

23 July 2021

Dr. Jai Gopal Pandey

Principal Scientist

CSIR-CEERI

Pilani, Rajasthan